



## Internship offer form

The following template provides the minimum information necessary to register and promote an internship in the framework of the Greenchips-EDU project. Partners may use their own template as long as they satisfy at least the information requested in this template.

Position ID	Internship on Silicongate Lab
Company	Silicongate Lda
Title	Mixed Signal Test Automation
Location	Silicongate Lda, R Alves Redol, 9
	1000-029 Lisboa, Portugal
Subject area <sup>1</sup>	Microelectronics engineering
Description	PCB design, test setup preparation, test script writing, characterization test debug, automatic test report production
Goals	Obtain basic skills on PCB design, programming a test sequence, analyzing test results and produce a test report
Start date	May 5, 2025
End date	June 29, 2025 (8 weeks)
Weekly workload	40
Grant	1000 euros (all included, after taxes)
Student profile	Bachelor (ongoing MSc)
Number of students	1
Minimum requirements	One course on power electronics. Basic knowledge of linear and switch regulators.
Desired requirements	Python, Altium software

Please note that in the case of Greenchips-EDU funded internships, the evaluation criteria must also be included in the form.

<sup>&</sup>lt;sup>1</sup> The subject area will be related or relevant to the topic of the GreenChips-EDU project. Examples are: *Electrical Engineering, Information and Computer Engineering, Microelectronic Engineering, Digital Engineering, Power Electronic Engineering, Sustainability related studies in the field of STEM studies*, etc.