



Internship offer form

The following template provides the minimum information necessary to register and promote an internship in the framework of the Greenchips-EDU project. Partners may use their own template as long as they satisfy at least the information requested in this template.

Position ID	Internship on Silicongate Lab
Company	Silicongate Lda
Title	22nm FDSOI Digital library design
Location	Silicongate Lda, R Alves Redol, 9
	1000-029 Lisboa, Portugal
Subject area¹	Microelectronics engineering
Description	Digital cells design and simulation
Goals	Design the schematic and layout of different cells of a digital library. The schematic must be validated by hspice simulation and the layout must be standard cell for future use in innovus
Start date	May 5, 2025
End date	June 29, 2025 (8 weeks)
Weekly workload	40
Grant	1000 euros (all included, after taxes)
Student profile	Bachelor (ongoing MSc)
Number of students	1
Minimum requirements	Basic electronics. Logic functions.
Desired requirements	Schematic and layout design experience.

Please note that in the case of Greenchips-EDU funded internships, the evaluation criteria must also be included in the form.

¹ The subject area will be related or relevant to the topic of the GreenChips-EDU project. Examples are: *Electrical Engineering, Information and Computer Engineering, Microelectronic Engineering, Digital Engineering, Power Electronic Engineering, Sustainability related studies in the field of STEM studies, etc.*